

Patent Abstracts of Japan

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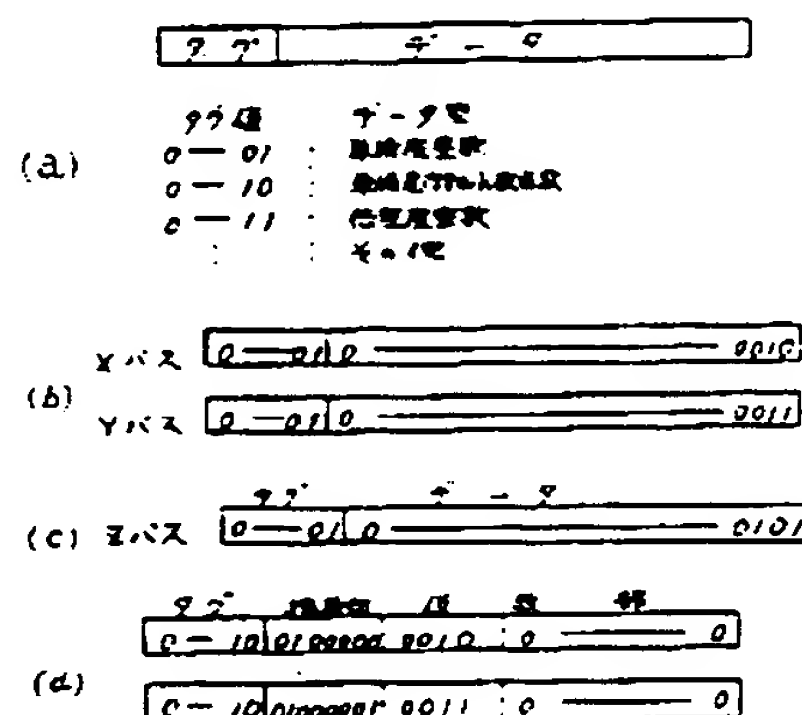
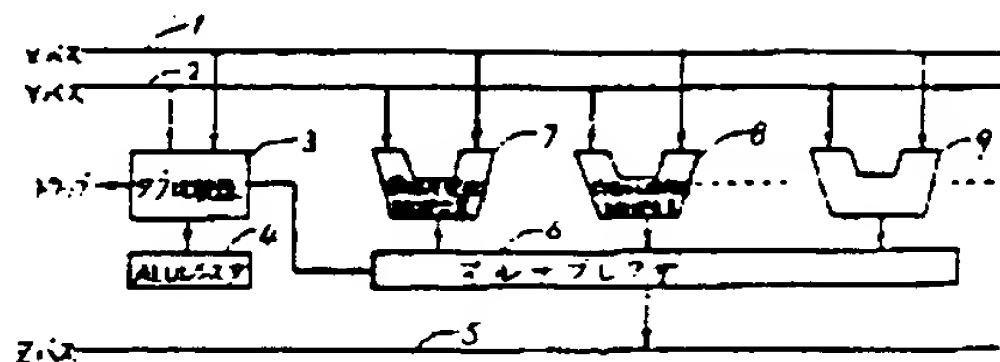
APPLICATION DATE : 22-12-86
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TITLE : FUNCTION TYPE PROCESSOR



ABSTRACT : **PURPOSE:** To carry out the data processing at a high speed regardless of any type of data by using a logical arithmetic unit which calculates exclusively plural specific data types and actuating plural logical arithmetic units at a time and in parallel with a tag chunk operation to select one of these arithmetic units based on the tag value of the data to be calculated.

CONSTITUTION: The logical arithmetic units 7~9 perform arithmetic operations of specific data types (single accuracy integers, single accuracy floating points) and outputs the results of these arithmetic operations to a multiplexer 6. The data to be calculated are outputted to data buses 1 and 2. At the same time, the tag field value of said data to be calculated is sent to a tag comparator 3 with the data field value sent to the units 7~9 respectively. The comparator 3 has comparison between the types of tag fields of said data to be calculated and informs the result of this comparison to the multiplexer 6. The multiplexer 6 selects the results of data types received from the comparator 3 out of plural received results and sends the selected result to a Z bus 5.

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